

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

B. Davis et al.

Serial No.:

09/752,861

Filed:

December 28, 2000

Group Art Unit:

2152

Examiner:

Unassigned

Title:

NUMA SYSTEM RESOURCE DESCRIPTORS INCLUDING

PERFORMANCE CHARACTERISTICS

RECEIVED

KECEIPT

Assistant Commissioner for Patents Washington, D.C. 20231

JUN 2 0 2001

Technology Center 2100

REQUEST FOR CORRECTED FILING RECEIPT

Applicant has received the official Filing Receipt for the above-referenced patent application, copy attached as Exhibit A. A corrected filing receipt is requested for the following point of information:

The Attorney Docket No. as typed on the Filing Receipt (i.e., BEA9-2000-0016-US1) is incorrect. A copy of the first page of the original application that was filed on December 28, 2000 is enclosed at Exhibit B which correctly states the Attorney Docket No. As BEA9-2000-0015-US1. Specifically, the number "0016" needs to be changed to "0015".

Applicant respectfully requests that a corrected filing receipt be issued and forwarded to applicant's attorney for this application.

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Serial No.:

09/752,861

Request for Corrected Filing Receipt

March 9, 2001



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FIL FEE REC'D ATTY. DOCKET. NO DRAWINGS **TOT CLAIMS** IND CLAIMS GRP ART UNIT FILING DATE APPLICATION NUMBER BEA9-2000-3 - 1 854 28 --12/28/2000 - 1 2152 09/752.861v⁻¹ 00(6)US1

CONFIRMATION NO. 1468

Pryor Garnett, Counsel **IBM Corporation** Intellectual Property Law Dept., EDO2-805 15450 SW Koll Parkway Beaverton, OR 97006-6063

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Date Mailed: 02/16/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

International Business Machines Corporation, Armonk, NY;

Continuing Data as Claimed by Applicant

Foreign Applications

If Required, Foreign Filing License Granted 02/15/2001

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Early Publication Request: No

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Title

Numa system resource descriptors including performance characteristics

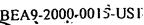
Preliminary Class

709

Data entry by : HAILE, MENTUAB

Team : OIPE

Date: 02/16/2001





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NUMA SYSTEM RESOURCE DESCRIPTORS INCLUDING PERFORMANCE CHARACTERISTICS

BACKGROUND OF THE INVENTION

Technical Field

This invention relates to a multiprocessor computer system and method for enhancing system performance. More specifically, the system provides for efficient allocation of system resources by determining latency between resources.

Description Of The Prior Art

Multiprocessor systems by definition contain multiple processors, also referred to herein as CPUs, that can execute multiple processes or multiple threads within a single process simultaneously in a manner known as parallel computing. In general, multiprocessor systems execute multiple processes or threads faster than conventional uniprocessor systems that can execute programs sequentially. The actual performance advantage is a function of a number of factors, including the degree to which parts of a multithreaded process and/or multiple distinct processes can be executed in parallel and the architecture of the particular multiprocessor system at hand. The degree to which processes can be executed in parallel depends, in part, on the extent to which they compete for exclusive access to shared memory resources.

The architecture of shared memory multiprocessor systems may be classified by how their memory is physically organized. In distributed shared memory (DSM) machines, the memory is divided into modules physically placed near one or more processors, typically on a processor node. Although all of the memory modules are globally accessible, a processor can access local memory on its node faster than remote memory on other nodes. Because the memory access time differs based on memory location, such systems are also called non-uniform memory access (NUMA) machines. In centralized shared memory machines, on the other hand, the memory is physically in one location.

Patent Application Specification





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Rib Data Sheet

CONFIRMATION NO. 1468

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APPLICANTS Brad A. Davis, Lake Oswego, OR;										
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